# STUDYING HARMONIC PROBLEMS USING A DESCRIPTOR SYSTEM APPROACH 

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#### Abstract

The harmonic voltage performance of a system depends on the location of its poles and zeros mainly with respect to the critical harmonic frequencies. Therefore the knowledge of the poles, zeros and their respective sensitivities to system parameters enables the identification of changes in the system which will reduce harmonic voltage levels. The method presented in [1], [2], based on state space formulation, allows this type of knowledge. Unfortunately, the construction of the state matrix for practical systems is not a simple task. Furthermore, the method in [1], [2] presents some limitations regarding network topology. This paper presents a method based on the descriptor system approach [3] which overcomes the computational difficulties associated with the state matrix method. The method properly deals with state variable redundancies and can be efficiently applied to large-scale networks of any topology.


Keywords: Descriptor System, Harmonics, Eigenvalues, Eigenvalue Sensitivities, Eigenvectors.

## 1. Introduction

Current programs for harmonic analysis in electrical systems require the inversion of the network admittance matrix, computed for numerous discrete values of frequency. This analysis is based on linear models and makes use of the superposition theorem, when considering multiple current harmonic sources [4], [5].

The main advantage of this method is that the admittance matrix is quite straightforward to build. This approach, however, does not allow the computation of the sensitivity of the resonance frequencies to the network parameters. Sensitivity analysis can play an important role in the elimination of undesirable resonances in the electrical system and as such it can be used as a powerful tool in harmonic studies [1], [2].

The method described in references [1] and [2], based on state variables, provides sensitivity analysis but the construction of the state matrix for practical systems is not a simple task. Furthermore the method assumes that two nodes can only be interconnected by an inductive element and that the capacitances are only connected between a node and the ground. Another limitation is that a capacitor bank must be modeled in every system bus.

The computational difficulties regarding the construction of the state matrix can be overcome with the use of the method proposed in [3], based on descriptor systems. The method properly deals with state variable redundancies and can be efficiently applied to large-scale networks of any topology.

The theoretical basis, comprising system modeling, harmonic impedance curves and sensitivity equations, will be presented in the next sections. Finally, results on a distribution system model will be described in order to validate and show the efficiency of the method.

## 2. Network Modeling

The behavior of any electrical network obeys three basic laws: Kirchoff law for currents, Kirchoff law for voltages and the inherent characteristics of each network element [6].

The Kirchoff laws contain the information on system topology and are represented by algebraic equations involving system variables (voltages and currents). Each algebraic equation determines a linear dependence among variables.

The system dynamics depends on the characteristic of its elements. In general, first order differential equations, in terms of currents and voltages, are used to represent inductive and capacitive elements. In this way, the inductive currents and capacitive voltages represent an obvious choice of state variables.

However, the construction of a dynamic model for the electrical network, based on state equations, is not so simple. By definition, the states form a minimum set of variables able to represent the dynamic behavior of a system [7]. In this way, a minimum set of inductive currents and capacitive voltages, which are linearly independent, must be determined.

This difficulty can be overcome by using the descriptor system (or partially dynamic system) to model the electrical network [3]. The network modeling by descriptor system uses all the inductive currents and all capacitive voltages as state variables. The algebraic constraints given by Kirchoff law for currents are also included in the model.

## 3. Single-phase RLC Series Branch

In general, the network modeling for studies regarding harmonic problems is done considering only the positive sequence network. For that reason, only single-phase models are necessary. Three-phase modeling for harmonic studies has been subject of research [8], but is not yet a common methodology for real system studies.

The single-phase RLC branch presented in Fig. 3.1, with frequency independent parameters, will be the basic element for network modeling in this work.


Fig. 3.1: RLC Series branch
The electrical behavior of this element can be described by a set of two ordinary differential equations of first order, as follows:

$$
\begin{gather*}
V_{k}-V_{j}=R \cdot I_{k j}+L \cdot \frac{d I_{k j}}{d t}+V_{C}  \tag{3.1}\\
C \cdot \frac{d V_{C}}{d t}=I_{k j} \tag{3.2}
\end{gather*}
$$

Equation (3.1) is general and holds for the particular cases where $L$ or $R$ are zero. However, when the parameter $C$ does not exist in the branch, (3.2) must be replaced by:

$$
\begin{equation*}
V_{C}=0 \tag{3.3}
\end{equation*}
$$

## 4. Descriptor System for Single-phase RLC Networks

In this work, a single-phase RLC network will be represented by the interconnection of several elements like that one shown in Fig. 3.1. For each element, (3.1) and (3.2) can be written in matrix form as:

$$
\left[\begin{array}{ll}
L & 0  \tag{4.1}\\
0 & C
\end{array}\right] \cdot\left[\begin{array}{l}
\dot{I}_{k j} \\
\dot{V}_{C}
\end{array}\right]=\left[\begin{array}{cc}
-R & -1 \\
1 & 0
\end{array}\right] \cdot\left[\begin{array}{l}
I_{k j} \\
V_{c}
\end{array}\right]+\left[\begin{array}{l}
1 \\
0
\end{array}\right] \cdot V_{k}+\left[\begin{array}{c}
-1 \\
0
\end{array}\right] \cdot V_{j}
$$

where the current $I_{k j}$ through the inductor and the voltage $V_{c}$ upon the capacitor were adopted as state variables. $V_{k}$ and $V_{j}$ are the voltages at nodes $k$ and $j$, respectively. The dot over the variable represents its time derivative.

If there is no capacitance, (4.1) must be replaced by:

$$
\left[\begin{array}{ll}
L & 0  \tag{4.2}\\
0 & 0
\end{array}\right] \cdot\left[\begin{array}{c}
\dot{I}_{k j} \\
\dot{V}_{C}
\end{array}\right]=\left[\begin{array}{cc}
-R & -1 \\
0 & 1
\end{array}\right] \cdot\left[\begin{array}{l}
I_{k j} \\
V_{c}
\end{array}\right]+\left[\begin{array}{l}
1 \\
0
\end{array}\right] \cdot V_{k}+\left[\begin{array}{c}
-1 \\
0
\end{array}\right] \cdot V_{j}
$$

Note that transforming (4.1) into (4.2) is simply done by setting the value of $C$ to zero and exchanging the elements in the last row of the state matrix.

It must be pointed out that $I_{k j}$ in (4.1) is a current from node $k$ to node $j$, i.e., a positive current injection at $j$ and negative at $k$. The interconnection among the several elements will be given by the equations regarding the Kirchoff law for the current applied to each system node: the algebraic summation of the injected currents at a node must be zero. Therefore, the current $I_{k j}$ must appear with a positive signal in the equation for the currents regarding the $j$ node and with a negative signal in the equation regarding the $k$ node. If the branch is connected to the ground ( $j=0$ or $k=0$ ), the current through this branch will only be present in one equation.

Therefore, the modeling of the electric network will yield two differential equations for each system RLC branch and an algebraic equation (regarding the Kirchoff law for the currents) for each system node.

After modeling all RLC branches, the resultant descriptor system will have the following matrix structure:

$$
\begin{gather*}
{\left[\begin{array}{c|c}
\mathbf{T}_{1} & \mathbf{0} \\
\hline \mathbf{0}^{T} & \mathbf{0}_{q}
\end{array}\right] \cdot\left[\frac{\dot{\mathbf{x}}_{1}}{\dot{\mathbf{v}}_{\text {nodal }}}\right]=\left[\begin{array}{l|l}
\mathbf{A}_{1} & \mathbf{A}_{2} \\
\hline \mathbf{A}_{3} & \mathbf{0}_{q}
\end{array}\right] \cdot\left[\frac{\mathbf{x}_{1}}{\mathbf{v}_{\text {nodal }}}\right]+\left[\begin{array}{l}
\mathbf{0} \\
\mathbf{I}
\end{array}\right] \cdot \mathbf{i}_{\text {nodal }}}  \tag{4.3}\\
\mathbf{v}_{\text {nodal }}=\left[\begin{array}{l|l}
\mathbf{0}^{T} & \mathbf{I}
\end{array}\right] \cdot\left[\frac{\mathbf{x}_{1}}{\mathbf{v}_{\text {nodal }}}\right] \tag{4.4}
\end{gather*}
$$

Considering $n_{l}$ as the number of RLC branches and $n_{n}$ as the number of nodes of the electric network, then $\mathbf{A}_{1}$ and $\mathbf{T}_{1}$ have dimension $2 \cdot n_{l} \times 2 \cdot n_{l}, \mathbf{A}_{2}$ and $\mathbf{0}$ have dimension $2 \cdot n_{l} \times n_{n}, \quad \mathbf{A}_{3}$ has dimension $n_{n} \times 2 \cdot n_{l}$ and $\mathbf{I}$ and $\mathbf{0}_{q}$ have dimension $n_{n} \times n_{n}$. The vector $\mathbf{x}_{1}$ has dimension $2 \cdot n_{l}$ and $\mathbf{v}_{\text {nodal }}$ and $\mathbf{i}_{\text {nodal }}$ have dimension $n_{n}$.
$\mathbf{T}_{1}$ is a diagonal matrix and $\mathbf{A}_{1}$ is block-diagonal, $\mathbf{A}_{2}$ and $\mathbf{A}_{3}$ are "incidence matrices" for the descriptor system. Symbols $\mathbf{0}$ and $\mathbf{0}_{q}$ denote zero matrices and $\mathbf{I}$ is the identity matrix.

The matrix equations (4.3) and (4.4) can be written in a more compact form as:

$$
\begin{gather*}
\mathbf{T} \cdot \dot{\mathbf{x}}=\mathbf{A} \cdot \mathbf{x}+\mathbf{B} \cdot \mathbf{u}  \tag{4.5}\\
\mathbf{y}=\mathbf{C} \cdot \mathbf{x} \tag{4.6}
\end{gather*}
$$

where:

$$
\left.\begin{array}{rlr}
\mathbf{T}=\left[\begin{array}{c|c}
\mathbf{T}_{1} & \mathbf{0} \\
\hline \mathbf{0}^{T} & \mathbf{0}_{q}
\end{array}\right] & \text { (4.7) } & \mathbf{A}=\left[\begin{array}{c|c}
\mathbf{A}_{1} & \mathbf{A}_{2} \\
\hline \mathbf{A}_{3} & \mathbf{0}_{q}
\end{array}\right] \\
\mathbf{B}=\left[\begin{array}{c}
\mathbf{0} \\
\hline \mathbf{I}
\end{array}\right] & \text { (4.9) } & \mathbf{C}=\left|\mathbf{0}^{T}\right| \mathbf{I} \tag{4.10}
\end{array}\right]
$$

Matrices $\mathbf{A}$ and $\mathbf{T}$ have dimension $\left(2 \cdot n_{l}+n_{n}\right) \times\left(2 \cdot n_{l}+n_{n}\right), \quad \mathbf{B} \quad$ has dimension $\left(2 \cdot n_{l}+n_{n}\right) \times n_{n}$ and $\mathbf{C}$ has dimension $n_{n} \times\left(2 \cdot n_{l}+n_{n}\right)$. Vector $\mathbf{x}$ has dimension $\left(2 \cdot n_{l}+n_{n}\right)$ and $\mathbf{y}$ and $\mathbf{u}$ have dimension $n_{n}$.

## 5. Harmonic Impedance seen from a System Node

Applying the Laplace Transform to (4.5), one has:

$$
\begin{equation*}
\mathbf{X}=(s \cdot \mathbf{T}-\mathbf{A})^{-1} \cdot \mathbf{B} \cdot \mathbf{U} \tag{5.1}
\end{equation*}
$$

where $\mathbf{X}$ and $\mathbf{U}$ are the Laplace transforms of $\mathbf{x}$ and $\mathbf{u}$, respectively.

The system impedance matrix, $\mathbf{Z}$, is defined by:

$$
\begin{equation*}
\mathbf{Y}=\mathbf{Z} \cdot \mathbf{U} \tag{5.2}
\end{equation*}
$$

where $\mathbf{Y}$ is the Laplace transform of $\mathbf{y}$.
Applying the Laplace transform to (4.6), one has:

$$
\begin{equation*}
\mathbf{Y}=\mathbf{C} \cdot \mathbf{X} \tag{5.3}
\end{equation*}
$$

Substituting (5.1) in (5.3), one has:

$$
\begin{equation*}
\mathbf{Y}=\mathbf{C} \cdot(s \cdot \mathbf{T}-\mathbf{A})^{-1} \cdot \mathbf{B} \cdot \mathbf{U} \tag{5.4}
\end{equation*}
$$

From (5.2) and (5.4), one has:

$$
\begin{equation*}
\mathbf{Z}=\mathbf{C} \cdot(s \cdot \mathbf{T}-\mathbf{A})^{-1} \cdot \mathbf{B} \tag{5.5}
\end{equation*}
$$

The self impedance seen from node $k$ is given by the $Z_{k k}$ element of the $\mathbf{Z}$ matrix. However, in accordance with (5.5) and with the equations numbered from (4.7) to (4.10) (definition equations of $\mathbf{A}, \mathbf{B}, \mathbf{C}$ and $\mathbf{T}$ ), one concludes that the $Z_{k k}$ element is equal to the $\left(2 \cdot n_{l}+k\right)$ diagonal element of $(s \cdot \mathbf{T}-\mathbf{A})^{-1}$. Thus:

$$
\begin{equation*}
Z_{k k}=\operatorname{diag}\left[(\mathrm{s} \cdot \mathbf{T}-\mathbf{A})^{-1}\right]_{\left(2 \cdot n_{l}+k\right)} \tag{5.6}
\end{equation*}
$$

On the other hand, the inverse of $(s \cdot \mathbf{T}-\mathbf{A})$ can be given by:

$$
\begin{equation*}
(s \cdot \mathbf{T}-\mathbf{A})^{-1}=\frac{\operatorname{adj}(s \cdot \mathbf{T}-\mathbf{A})}{\operatorname{det}(s \cdot \mathbf{T}-\mathbf{A})} \tag{5.7}
\end{equation*}
$$

Consider $\mathbf{T}_{k}$ and $\mathbf{A}_{k}$ as the matrices obtained by canceling the $2 \cdot n_{l}+k$ row and column of the matrices $\mathbf{T}$ and $\mathbf{A}$, respectively. Thus, the $2 \cdot n_{l}+k$ diagonal element of $(s \cdot \mathbf{T}-\mathbf{A})^{-1}$ is given by:

$$
\begin{equation*}
Z_{k k}=\operatorname{diag}\left[(\mathrm{s} \cdot \mathbf{T}-\mathbf{A})^{-1}\right]_{\left(2 \cdot n_{l}+k\right)}=\frac{\operatorname{det}\left(s \cdot \mathbf{T}_{k}-\mathbf{A}_{k}\right)}{\operatorname{det}(s \cdot \mathbf{T}-\mathbf{A})} \tag{5.8}
\end{equation*}
$$

Equation (5.8) is a generalization of its counterpart presented in [1] and [2]. It shows that:

- The system poles correspond to the generalized eigenvalue problem associated with the matrix pair $\{\mathbf{A}, \mathbf{T}\}$ :

$$
\begin{equation*}
\operatorname{det}(s \cdot \mathbf{T}-\mathbf{A})=0 \Leftrightarrow \mathbf{A} \cdot \mathbf{v}_{i}=\lambda_{i} \cdot \mathbf{T} \cdot \mathbf{v}_{i} \tag{5.9}
\end{equation*}
$$

- The zeros, associated with the self impedance of node $k$, correspond to the generalized eigenvalue problem associated with the matrix pair $\left\{\mathbf{A}_{k}, \mathbf{T}_{k}\right\}$ :

$$
\begin{equation*}
\operatorname{det}\left(s \cdot \mathbf{T}_{k}-\mathbf{A}_{k}\right)=0 \Leftrightarrow \mathbf{A}_{k} \cdot \mathbf{v}_{i}=\lambda_{i} \cdot \mathbf{T}_{k} \cdot \mathbf{v}_{i} \tag{5.10}
\end{equation*}
$$

where $\lambda_{i}$ are the generalized eigenvalues associated with the pair $\{\mathbf{A}, \mathbf{T}\}$ or $\left\{\mathbf{A}_{k}, \mathbf{T}_{k}\right\}$ and $\mathbf{v}_{i}$ are their associated generalized eigenvectors.

## 6. Example of Electric Network Modeling

A simple RLC circuit with state redundancy (see Fig. 6.1) will be used to describe the network modeling, by means of the descriptor system technique.


Fig. 6.1: RLC Circuit with state redundancy
The state redundancy of this RLC circuit was artificially introduced by splitting its inductance into two parts.

It must be pointed out that the RLC circuit shown in Fig. 6.1 is a particular case of the RLC circuit shown in Fig. 6.2. When $C_{12}$ and $C_{2}$ are neglected and
$R_{1}=L_{1}=R_{2}=0, C_{1}=C, R_{12}=R, L_{12}=L$ and $L_{2}=L^{\prime}$ the circuit shown in Fig. 6.2 becomes equivalent to that shown in Fig. 6.1.

For an easier understanding, the modeling of the circuit shown in Fig. 6.1will be derived from the modeling of the circuit shown in Fig. 6.2.

In this modeling, the inductive currents and capacitive voltages are chosen as state variables. A matrix equation in the form of (4.1) is written for each branch. The branch connections are taken into account by the Kirchoff law for the currents applied to the circuit nodes.

For the circuit shown in Fig. 6.2 one has:

$$
\begin{gather*}
i-I_{L 1}-I_{L 12}=0  \tag{6.1}\\
I_{L 12}-I_{L 2}=0 \tag{6.2}
\end{gather*}
$$



Fig. 6.2: Generalized RLC circuit
Joining the matrix equations relative to each circuit branch and (6.1) and (6.2), yield:

$$
\left[\begin{array}{cc|cc|cc|cc}
L_{1} & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & C_{1} & 0 & 0 & 0 & 0 & 0 & 0 \\
\hline 0 & 0 & L_{2} & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & C_{2} & 0 & 0 & 0 & 0 \\
\hline 0 & 0 & 0 & 0 & L_{12} & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & C_{12} & 0 & 0 \\
\hline 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0
\end{array}\right] \cdot\left[\begin{array}{c}
\dot{I}_{L 1} \\
\frac{\dot{V}_{C 1}}{\dot{I}_{L 2}} \\
\dot{V}_{C 2} \\
\frac{\dot{I}_{L 12}}{\dot{V}_{C 12}} \\
\hline \dot{V}_{1} \\
\dot{V}_{2}
\end{array}\right]=
$$

$\left[\begin{array}{cc|cc|cc|cc}-R_{1} & -1 & 0 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & -R_{2} & -1 & 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & 0 & -R_{12} & -1 & 1 & -1 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ \hline-1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 \\ 0 & 0 & -1 & 0 & 1 & 0 & 0 & 0\end{array}\right] \cdot\left[\begin{array}{c}I_{L 1} \\ \frac{V_{C 1}}{I_{L 2}} \\ \frac{V_{C 2}}{I_{L 12}} \\ \frac{V_{C 12}}{V_{1}} \\ V_{2}\end{array}\right]+\left[\begin{array}{c}0 \\ 0 \\ \frac{0}{0} \\ \frac{0}{0} \\ 0 \\ \frac{0}{1} \\ 0\end{array}\right] \cdot i(t)$

The node voltage vector is given by:

$$
\mathbf{y}=\left[\begin{array}{l|l|l|l|l|l|l|l}
0 & 0 & 0 & 0 & 0 & 0 & 1 & 0  \tag{6.4}\\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\
1
\end{array}\right] \cdot\left[\begin{array}{c}
\frac{I_{L 1}}{\frac{V_{C 1}}{I_{L 2}}} \\
\frac{V_{C 2}}{I_{L 12}} \\
\frac{V_{C 12}}{V_{1}} \\
V_{2}
\end{array}\right]
$$

The model of the circuit shown in Fig. 6.1 is accomplished simply by setting $R_{1}=L_{1}=R_{2}=0$ and
neglecting $C_{12}$ and $C_{2}$ which, regarding the items 3 and 4 , implies in modifying (6.3) to:

$$
\left[\begin{array}{cc|cc|cc|cc}
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & C_{1} & 0 & 0 & 0 & 0 & 0 & 0 \\
\hline 0 & 0 & L_{2} & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\hline 0 & 0 & 0 & 0 & L_{12} & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\hline 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0
\end{array}\right] \cdot\left[\begin{array}{c}
\dot{I}_{L 1} \\
\dot{V}_{C 1} \\
\hline \dot{I}_{L 2} \\
\dot{V}_{C 2} \\
\frac{\dot{I}_{L 12}}{} \\
\dot{V}_{C 12} \\
\hline \dot{V}_{1} \\
\dot{V}_{2}
\end{array}\right]=
$$

$\left[\begin{array}{cc|cc|cc|cc}0 & -1 & 0 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & -1 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & 0 & -R_{12} & -1 & 1 & -1 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ \hline-1 & 0 & 0 & 0 & -1 & 0 & 0 & 0 \\ 0 & 0 & -1 & 0 & 1 & 0 & 0 & 0\end{array}\right] \cdot\left[\begin{array}{c}I_{L 1} \\ \frac{V_{C 1}}{I_{L 2}} \\ \frac{V_{C 2}}{I_{L 12}} \\ V_{C 12} \\ \hline V_{1} \\ V_{2}\end{array}\right]+\left[\begin{array}{c}0 \\ 0 \\ \frac{1}{0} \\ \frac{0}{0} \\ 0 \\ 0 \\ 1 \\ 0\end{array}\right] \cdot i(t)$
where

$$
\begin{array}{ll} 
& I_{L 1}=I_{C} \\
C_{1}=C & I_{L 12}=I_{L} \\
R_{12}=R & I_{L 2}=I_{L^{\prime}}  \tag{6.6}\\
L_{12}=L & V_{C 1}=V_{1} \\
L_{2}=L^{\prime} & V_{C 12}=V_{C 2}=0
\end{array}
$$

The dynamic model of the circuit shown in Fig. 6.1, is therefore, given by (6.4) and (6.5), considering the relations given in (6.6).

## 7. Eigenvalue Sensitivity

Considering $\lambda_{1}, \lambda_{2}, \ldots, \lambda_{n}$ as the generalized eigenvalues associated with the matrices $\{\mathbf{A}, \mathbf{T}\}$, one has:

$$
\begin{equation*}
\mathbf{A} \cdot \mathbf{v}_{i}=\lambda_{i} \cdot \mathbf{T} \cdot \mathbf{v}_{i} \tag{7.1}
\end{equation*}
$$

where $\mathbf{v}_{i}$ is the right (column) eigenvector of $\{\mathbf{A}, \mathbf{T}\}$ associated with the eigenvalue $\lambda_{i}$.

Similarly, the left (row) eigenvector $\mathbf{w}_{i}$ associated with the eigenvalue $\lambda_{i}$ can be defined by:

$$
\begin{equation*}
\mathbf{w}_{i} \cdot \mathbf{A}=\lambda_{i} \cdot \mathbf{w}_{i} \cdot \mathbf{T} \tag{7.2}
\end{equation*}
$$

Differentiating (7.1) with respect to $\alpha_{j}$ (a system parameter) yields:

$$
\begin{equation*}
\frac{\partial \mathbf{A}}{\partial \alpha_{j}} \cdot \mathbf{v}_{i}+\mathbf{A} \cdot \frac{\partial \mathbf{v}_{i}}{\partial \alpha_{j}}=\frac{\partial \lambda_{i}}{\partial \alpha_{j}} \cdot \mathbf{T} \cdot \mathbf{v}_{i}+\lambda_{i} \cdot \frac{\partial \mathbf{T}}{\partial \alpha_{j}} \cdot \mathbf{v}_{i}+\lambda_{i} \cdot \mathbf{T} \cdot \frac{\partial \mathbf{v}_{i}}{\partial \alpha_{j}} \tag{7.3}
\end{equation*}
$$

Premultiplying (7.3) by $\mathbf{w}_{i}$ and using (7.2), one obtains

$$
\begin{equation*}
\frac{\partial \lambda_{i}}{\partial \alpha_{j}}=\frac{1}{C_{i}} \cdot \mathbf{w}_{i} \cdot\left(\frac{\partial \mathbf{A}}{\partial \alpha_{j}}-\lambda_{i} \cdot \frac{\partial \mathbf{T}}{\partial \alpha_{j}}\right) \cdot \mathbf{v}_{i} \tag{7.4}
\end{equation*}
$$

where:

$$
\begin{equation*}
C_{i}=\mathbf{w}_{i} \cdot \mathbf{T} \cdot \mathbf{v}_{i} \tag{7.5}
\end{equation*}
$$

Equation (7.4) is a generalization of the well-known sensitivity equation for the eigenvalue $\lambda_{i}$ with respect to a system parameter $\alpha_{j}$ [1], [2].

In most cases, the normalization of the sensitivities is useful. This is obtained by multiplying the eigenvalue sensitivity $\frac{\partial \lambda_{i}}{\partial \alpha_{j}}$ by the system parameter $\alpha_{j}$ [1], [2].

Let $\alpha_{j}^{0}$ be the initial value of the system parameter $\alpha_{j}$. Thus the variation of the eigenvalue $\lambda_{i}$ as a function of the parameter variation $\Delta \alpha_{j}$ and of the normalized sensitivity is given in a first approximation by:

$$
\begin{equation*}
\Delta \lambda_{i}=\alpha_{j}^{0} \cdot \frac{\partial \lambda_{i}}{\partial \alpha_{j}}\left(\alpha_{j}^{0}\right) \cdot \frac{\Delta \alpha_{j}}{\alpha_{j}^{0}} \tag{7.6}
\end{equation*}
$$

The symbol $\frac{\partial \lambda_{i}}{\partial \alpha_{j}}\left(\alpha_{j}^{0}\right)$ denotes that the eigenvalue sensitivity has been computed for a parameter value of $\alpha_{j}^{0}$.

Equation (7.6) can be easily generalized for simultaneous variation of several system parameters.

## 8. Test System

The test system utilized has been taken from [2], and is shown in Fig. 8.1.

This system can be modeled by the interconnection of several series RLC branches, as shown in Fig. 8.2.

The system frequency is 50 Hz and the values of its elements are given in Table 8.1. $L_{12}$ and $R_{12}$ represent the inductance and resistance of the equivalent series association of the line $L T$ 1-2 with the transformer $T 2$. Similarly, $L_{13}$ and $R_{13}$ represent the inductance and resistance of the equivalent series association of the line LT 1-3 with the transformer T3.


Fig. 8.1: Test system

[^0]

Fig. 8.2: System modeling
Table 8.1: System parameter values

| Inductance (mH) |  | Resistance ( $\Omega$ ) |  | Capacitance ( $\mu \mathrm{F}$ ) |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $L_{c c}$ | 8.0 | $R_{2}$ | 80.0 | $C_{1}$ | 23.9 |
| $L_{2}$ | 424.0 | $R_{3}$ | 133.0 | $C_{2}$ | 8.0 |
| $L_{3}$ | 531.0 | $R_{12}$ | 0.46 | $C_{3}$ | 11.9 |
| $L_{12}$ | 9.7 | $R_{13}$ | 0.55 |  |  |
| $L_{13}$ | 11.9 |  |  |  |  |

The matrices $\mathbf{A}$ and $\mathbf{T}$ have dimension $2 \cdot n_{l}+n_{n}$, where $n_{l}$ represents the number of RLC branches and $n_{n}$ the number of nodes of the electric network. For the test system, the order these matrices is 23 . The sparse structure of the generalized state matrix ( $\mathbf{A}$ ), for the test system, is depicted in Fig. 8.3.

The constant nz represents the number of nonzero elements, and is shown at the bottom of Fig. 8.3.


Fig. 8.3: Sparse structure of matrix $\mathbf{A}$ for Test System

### 8.1 Calculation of Poles, Zeros and Sensitivities

Although the order of the matrices $\mathbf{A}$ and $\mathbf{T}$ is 23 , the test system is actually of eighth-order. Therefore, 15 generalized eigenvalues of infinite modulus corresponding to the algebraic equations are obtained.

For matrices $\mathbf{A}_{k}$ and $\mathbf{T}_{k}$ there are also 15 generalized eigenvalues of infinite modulus but only 7 of finite modulus. The values of the finite generalized eigenvalues associated to $\{\mathbf{A}, \mathbf{T}\}$ (poles) and to $\left\{\mathbf{A}_{k}, \mathbf{T}_{k}, k=1,2,3\right\}$ (zeros), are shown in Table 8.2.

The frequencies of poles (parallel resonance) and zeros (series resonance), given by the imaginary part of the complex conjugate eigenvalues, and their sensitivities with respect to the inductances and capacitances of the studied system are presented in Table 8.3. Note there is a $2 \pi$ factor between the frequency values shown in Table 8.2 and Table 8.3, since the latter is given in Hz .

Table 8.2: Generalized eigenvalues

| $\{\mathbf{A}, \mathbf{T}\}$ | $\left\{\mathbf{A}_{1}, \mathbf{T}_{1}\right\}$ | $\left\{\mathbf{A}_{2}, \mathbf{T}_{2}\right\}$ | $\left\{\mathbf{A}_{3}, \mathbf{T}_{3}\right\}$ |
| :---: | :---: | :---: | :---: |
| -345.9 | -338.5 | -93.7 | -398.4 |
| $\pm j 4535.6$ | $\pm j 2670.9$ | $\pm j 3975.6$ | $\pm j 4424.9$ |
| -507.0 | -804.4 | -255.5 | -415.3 |
| $\pm j 3069.1$ | $\pm j 3550.6$ | $\pm j 2084.9$ | $\pm j 2402.1$ |
| -290.1 | -1.0 | -26.2 | -27.8 |
| $\pm j 1583.6$ |  |  |  |
| -1.0 | -1.1 | -1.0 | -1.0 |
| -1.0 | 0.0 | 0.0 | 0.0 |

Table 8.3: Resonance frequency sensitivities

|  | Poles |  |  | Zeros |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Node 1 |  |  | Node 2 |  | Node 3 |  |  |  |  |
|  | 1 | 2 | 3 | 1 | 2 | 1 | 2 | 1 | 2 |  |
| $f(H z)$ | 252 | 488 | 722 | 425 | 565 | 332 | 633 | 382 | 704 |  |
| $L_{c c}$ | 633 | 68 | 312 | 0 | 0 | 302 | 501 | 550 | 292 |  |
| $L_{2}$ | 18 | 22 | 11 | 0 | 41 | 0 | 0 | 33 | 15 |  |
| $L_{3}$ | 22 | 12 | 1 | 30 | 0 | 29 | 5 | 0 | 0 |  |
| $L_{12}$ | 11 | 493 | 1551 | 0 | 1820 | 248 | 415 | 232 | 1819 |  |
| $L_{13}$ | 119 | 949 | 492 | 1324 | 0 | 470 | 1080 | 368 | 199 |  |
| $C_{1}$ | 284 | 74 | 1295 | 0 | 0 | 237 | 1523 | 533 | 1188 |  |
| $C_{2}$ | 158 | 732 | 697 | 0 | 1689 | 0 | 0 | 685 | 912 |  |
| $C_{3}$ | 339 | 719 | 178 | 1317 | 0 | 799 | 452 | 0 | 0 |  |

It must be pointed out that all the results presented here are in good agreement with those presented in [2].

### 8.2 Shifting Poles and Zeros

As shown in Table 8.3 the pole 1 is located at 252 Hz . It can cause problems at any system bus since an injection of fifth-harmonic current can generate high levels of harmonic distortions.

A possible solution consists in bringing the zero 1 seen from node 2 closer to the frequency of 250 Hz .

The highlighted sensitivity results in Table 8.3 indicate that changes in parameter $C_{3}$ will cause the largest shifts in the chosen zero.

Using (7.6) a value of $C_{3}=19.56 \mu \mathrm{~F}$ was obtained.
The impedance magnitudes seen from bus 2 associated with the original and the new value of $C_{3}$ are shown in Fig. 8.4.

The frequencies of the poles and zeros, for the new value of $C_{3}$, are presented in Table 8.4. As shown in this table, the frequency of the pole 1 seen from node 2 is 270 Hz instead of 250 Hz . A more accurate technique for shifting poles and zeros based on Newton-Raphson method is currently being developed.

For this new value of $C_{3}$ there was a reduction of $47 \%$ in the impedance magnitude at 250 Hz .

Table 8.4: New frequencies of Poles and Zeros

|  |  |  |  | Poles |  | Zeros |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Node 1 |  | Node 2 |  | Node 3 |  |  |  |  |  |  |
| $f(H z)$ | 1 | 2 | 3 | 1 | 2 | 1 | 2 | 1 | 2 |  |  |
|  | 221 | 442 | 713 | 332 | 565 | 270 | 606 | 382 | 704 |  |  |



Fig. 8.4: Impedance modulus seen from node 2

## 9. Conclusion

The state space method [1], [2] and the proposed descriptor system method allow obtaining all the results produced by the traditional method which is based on nodal admittance matrices computed at various discrete values of frequency within the range of interest. Furthermore, these methods allow:

- Identification of elements mostly involved in specific resonances.
- Determination of the necessary changes in system elements in order to shift the location of poles and/or zeros to desired positions.
- Optimum allocation of capacitor banks and/or passive filters.
The state space method in [1], [2] has, however, some limitations regarding its ability to model practical networks. The proposed descriptor system method overcomes these limitations and offers the following advantages:
- Simple and efficient computational implementation.
- Ability to model systems of any topology and containing state variable redundancies.
- Applicability to large-scale networks, due to the very sparse matrices involved and the availability of powerful sparse eigensolution algorithms applied to descriptor systems [3], [9], [10].


## 10. Bibliography

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[^0]:    $V_{t h} \quad$ : Thévenin voltage.
    $L_{c c} \quad$ : Short-circuit inductance of the HV system.
    $T 1$ : HV/MV Transformer.
    T2, T3 : MV/LV Transformers.
    TL 1-2 : Transmission line connecting node 1 to transformer $T 2$.
    TL 1-3 : Transmission line connecting node 2 to transformer $T 3$.
    $C_{1}, C_{2}, C_{3}$ : Capacitor banks connected to nodes 1,2 and 3, respectively.
    $Z_{2}, Z_{3} \quad$ : Load impedances connected to nodes 2 and 3, respectively.
    $I_{h 1}, I_{h 2}, I_{h 3} \quad$ : Harmonic current sources connected to nodes 1, 2 and 3, respectively.

