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VIII SYMPOSIUM OF SPECIALISTS IN ELECTRIC OPERATIONAL AND EXPANSION PLANNING

Preliminary Studies of Coordinated Voltage Control Applied to the Rio Area

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Introduction

- Description of Coordinated Voltage Control (CVC)
- Fast simulator based on "quasi-steady-state" concept
- Results for 24-hour load-curve in the Rio Area
- Fast simulations compared with load flow results
- Final Remarks

Coordinated Voltage Control (CVC)

- > Primary voltage regulation (PVR)
 - → Generator control (AVR)
 - → Joint voltage control (JVC) ?
- Secondary voltage regulation (SVR)
 - Pilot node concept
 - → Reference voltage changes in generator AVRs, etc.
- > Tertiary voltage regulation (TVR)
 - → Open loop control
 - Provides the voltage reference values to the SVR via an OPF program
 - Requires system data acquisition (SCADA)

Hierarchical Structure of Coordinated Voltage Control



Main Objectives of Secondary Voltage Regulation (SVR)

- Pilot bus voltage regulation
- Reactive power sharing among the participating power plants
- Better transmission voltage regulation
- Better voltage performance following contingencies or changes in topology or loading

Fast Simulator of SVR Dynamics

- > SVR dynamic behavior is modeled by differential equations
- The fast dynamics are assumed instantaneous and stable
- > System frequency dynamics is neglected
- > Simulator only captures mid and long-term dynamics

Fast Simulator

Comparing results from a transient stability program and a fast simulation program





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> Voltage profile without SVR



> Voltage profile with SVR



> Pilot node voltage profile



> Enlarged view of pilot bus voltage after a load decrease



> Angra I voltage profile



> Furnas voltage profile





> Furnas reactive power



> S. Cruz reactive power



> Pilot Node voltage is regulated at 0.97 p.u.



> Angra I Terminal Voltage



> Angra I reactive power



Bus #10 - Angra I

> Total Reactive Power



Total Reactive Power

Validation of Fast Simulation Results

- Small differences in results are due to different bus representations in the two programs:
 - → Power flow (Anarede): buses represented as Vθ, PV and PQ
 - Fast simulator (FastSim): all buses have a PQ model and generator voltage regulators modeled by their algebraic equations
- Proper PV bus representation in FastSim is desirable

Final Remarks

- > This coordinated voltage control structure is similar to those used in first European schemes
- Coordinated Voltage Control schemes improve the overall system performance
 - Improved transmission voltage profile
 - → Better utilization of reactive power resources
 - Proper VAr Sharing among generating plants
 - Lower number of equipment switchings (tap changers and shunt banks)